


U.S. Department of Commerce, Patent and Trademark Office	Application No.: 10/601,237
	Filing date: June 19, 2003
	Inventors: Laurence B. Boucher, et al.
	Group Art Unit: 2143
	Examiner name: Jerry B. Dennison
ATTORNEY DOCKET NO. ALA-008F	

## HIGH PERFORMANCE NETWORK INTERFACE

## U.S. Patent Documents

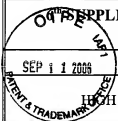
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	1	5,418,912	5-95	Christenson	709	234	
	2	5,535,375	7-96	Eshel	709	27	
	3	5,633,780	05-1997	Cronin	361	220	
	4	5,682,534	10-97	Kapoor et al.	709	220	
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	8	5,872,919	2-99	Wakeland	709	235	
	9	5,892,903	04-1999	Klaus	709	224	
	10	5,920,566	07-1999	Hendel et al.	370	401	
	11	5,970,804	10/26/99	Osborne	370	200.75	
	12	6,041,058	09-2000	Flanders et al.	370	474	
	13	6,049,528	4-00	Hendel et al.	370	235	
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	19	6,223,242	09-2000	Mayer et al.	370	392	
	20	6,289,023	09-2000	Dowling et al.	370	419	
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	23	6,427,169	07/30/02	Elzur	709	224	
	24	6,487,654	11-02	Dowling	709	234	
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	26	6,591,310	7-03	Johnson	709	3	
	27	6,650,640	11-2003	Muller et al.	370	392	

Examiner /Jerry Dennison/

Date Considered 03/22/2010

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. J.D./

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 <b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	Filing date: June 19, 2003
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## U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	28	6,681,364	1-04	Calvignac et al.	714	776
	29	6,697,868	02-2004	Craft et al.	709	230
	30	6,807,581	10-2004	Starr et al.	709	250
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	33	2003/0165160	9/4/03	Minami et al.	370	466
	34	2004/0054814	3-04	McDaniel	709	250
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## Foreign Patent Documents

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	Document Number	Date	Country	Class	Subclass	Yes	No
A	WO 01/59966	8/16/01	PCT/US00/06475				
B	WO 01/86430	11/15/01	PCT/US01/15180				

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

1	iSCSI HBA article entitled "iSCSI and 2Gigabit fibre Channel Host Bus Adapters from Emulex, QLogic, Adaptec, JNI", 8 pages, printed 10/01/01.
2	U.S. Provisional Patent Application No.: 60/283,896, Titled: CRC Calculations for Out of Order PUDs, Filed April 12, 2003, Inventor: Amit Oren, Assignee: Siliquent Technologies Ltd.
3	WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.
4	WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.
5	Intel article entitled "Solving Server Bottlenecks with Intel Server Adapters", Copyright Intel Corporation, 1999, 8 pages.
6	Thia, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", <u>Protocols for High Speed Networks</u> , pages 53-68, 1993.
7	Thia, Y.H. Publication entitled "A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", <u>Protocols for High Speed Networks</u> , pages 224-239, 1995.

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